

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
EXAMINATION BRANCH
M.TECH II SEMESTER (REGULAR & SUPPLEMENTRY) EXAMINATIONS, AUG/SEPT-2009

Time: 10 A.M to 1.00 P.M

M.Tech Time Table for Mechanical, Chemical & Civil				
Date & Day	MACHINE DESIGN	CAD/CAM	CHEMICAL	CIVIL
31-08-2009 (Monday)	Advanced Mechanical Engineering Design	Computer Aided Manufacturing	Advanced Separation processes	Construction Economics, Finance Management and Control
02-09-2009 (Wednesday)	Mechanical Vibrations	Flexible Manufacturing Systems	Advanced Process Control	Construction Personal Management Quality Control and Safety Management
04-09-2009 (Friday)	Design Synthesis	Industrial Robotics	Process Modeling and Simulation	Construction Planning Scheduling and Management
07-09-2009 (Monday)	Experimental Stress Analysis	Design of Hydraulics and Pneumatics Systems	Optimization Techniques in Chemical Engineering	Environmental Impact Assessment
09-09-2009 (Wednesday)	Optimization Techniques	Automation in Manufacturing	Membrane Technology	Construction Methods and Equipment
11-09-2009 (Friday)	Computational Fluid Dynamics	1.Computational Fluid Dynamics 2.Computer Aided Process Planning 3.Intelligent Manufacturing systems	Computational Fluid Dynamics.	Contracts and Arbitration
14-09-2009 (Monday)	1.Gear Engineering 2. Mechanics of Composite Materials	-----	-----	-----

NOTE: If Government declares holiday on any of the above dates, the examinations will be conducted as usual.

M.Tech Time Table for CSE				
Date & Day	Software Engineering	Computer Science and Engineering	Information Technology	Neural Networks
31-08-2009 (Monday)	Information Security	Object Oriented Analysis and Design	Internetworking with TCP/IP	Expert Systems
02-09-2009 (Wednesday)	Data Ware Housing and Mining	Data Ware Housing and Mining	Network Management Systems	Data Mining & Data Ware Housing
04-09-2009 (Friday)	Software Quality Assurance and Testing	Network Security and Cryptography	Design Patterns	Functional Programming
07-09-2009 (Monday)	Software Architecture and Design Pattern	Embedded Systems	-----	Machine Learning
09-09-2009 (Wednesday)	1. Human Computer Interaction 2. Image Processing and Pattern Recognition	1. Neural Networks 2. Advanced Computer Architecture 3. Pattern Recognition and Image Processing	Human Computer Interaction	Pattern Recognition
11-09-2009 (Friday)	1. Information Retrieval Systems 2. Multimedia and Application Development	1. Software Project Management 2. Mobile Computing	Information Retrieval Systems	Object Oriented Software Engineering
14-09-2009 (Monday)	Middle Ware Technologies	Middle Ware Technologies	-----	-----

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M.Tech Time Table for EEE

Date & Day	Power & Industrial Drives	Power Electronics	PSC & A	Power Systems (High Voltage)	Power Electronics & Electric Drives
31-08-2009 (Monday)	Power Electronics Control of AC Drives	Power Electronics Control of AC Drives	Power System Control & Stability	High Voltage Testing Techniques	Power Electronics Control of AC Drives
02-09-2009 (Wednesday)	Flexible AC Transmission Systems	Flexible AC Transmission Systems	Flexible AC Transmission Systems	Flexible AC Transmission Systems	Flexible AC Transmission Systems
04-09-2009 (Friday)	Dynamics of Electrical Machines	Micro Processors & Micro Controllers	Extra High Voltage Transmission	Partial Discharge in HV Equipment	Dynamics of Electrical Machines
07-09-2009 (Monday)	High Voltage DC Transmission	Neural & Fuzzy Systems	Neural & Fuzzy Systems	Surge Phenomena and Insulation Coordination	High Voltage DC Transmission
09-09-2009 (Wednesday)	1.Programmable Logic Controllers & Their Applications 2.Advanced Digital Signal Processing	1.Special Machines & Control 2.Enterprise Resource Planning	Real Time Control of Power Systems	Power System Protection with Static Relays	1.Programmable Logic Controllers & Their Applications 2.Advanced Digital Signal Processing
11-09-2009 (Friday)	1.Power Quality 2. Reliability Engineering	1.Power Quality 2. Digital Control Systems	Power Quality	Power System Operation Control	Reliability Engineering

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M.Tech Time Table for ECE

M.Tech Time Table for ECE							
Date & Day	VLSI SYSTEM DESIGN/ VLSI DESIGN	VLSI & EMBEDDED SYSTEMS	EMBEDDED SYSTEMS	DIGITAL ELECTERONICS & COMMUNICATI ON SYSTEMS	DIGITAL SYSTEMS & COMPUTER ELECTRONICS	ELECTRONICS & COMMUNICATION ENGINEERING	SYSTEMS & SIGNALPROC ESSING
31-08-2009 (Monday)	Algorithms for VLSI Design Automation	Algorithms for VLSI Design Automation	Embedded Software Design	Speech Processing	Speech Processing	Telecommunication Switching	Speech Processing
02-09-2009 (Wednesday)	Design For Testability	Real Time Operating Systems For Embedded Systems	1. Real Time Operating Systems For Embedded Systems 2. Design For Testability	Image Processing	Image Processing	Image & Video Compression	Image Processing
04-09-2009 (Friday)	1.Network Security and Cryptography 2. Hardware, Software Co-Design 3. System Modeling & Simulation	1.Network Security and Cryptography 2.Advanced Digital Filtering	1.Wireless Communication & Networks 2.Advanced Digital Filtering	Wireless Communication & Networks	Wireless Communication & Networks	Cryptography & Network Security	Wireless Communication & Networks
07-09-2009 (Monday)	Low Power VLSI Design	VLSI Technology and Design	Low Power VLSI Design	1.Low Power VLSI Design 2.High Speed Networks	1.Low Power VLSI Design 2.High Speed Networks	Advanced Digital IC Design	Adaptive Signal Processing
09-09-2009 (Wednesday)	Scripting Language for VLSI Design Automation	CPLD and FPGA architecture and Applications	CPLD and FPGA architecture and Applications	Coding Theory & Techniques	Embedded & Real Time Systems	Embedded & Real Time Systems	Coding Theory & Techniques

11-09-2009 (Friday)	1.DSP Processors & Architectures 2. Advanced Operating Systems	DSP Processors & Architectures	DSP Processors & Architectures	1. DSP Processors & Architectures 2.Design of Fault Tolerant Systems	Design of Fault Tolerant Systems	Computational Intelligence	-----
14-09-2009 (Monday)	-----	Image and Video Processing	-----	Micro Computer System Design	-----	-----	-----
16-09-2009 (Wednesday)	-----	-----	-----	Radar Signal Processing	-----	-----	Radar Signal Processing

**NOTE: 1. If Government declares holiday on any of the above dates, the examinations will be conducted as usual.
2. For any other clarification in respect of the above examinations please contact additional controller of Examinations-2 through email ace2@jntukakinada.edu.in on or before 18-08-2009.**

DATE: 17-08-2009


Director of Evaluation